

Advanced Technology Microcircuit (ATM) QML Development

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The desire to leverage heterogeneous integrated products and devices leveraging multiple technology nodes, through silicon via (TSV) and various fan out technologies has driven the need to generate a new qualification document for DLA land and maritime to enable these technologies to be incorporated into the qualified manufacturer listing (QML) program. The MIL-PRF-ATM document is being developed under the auspices of the JEDEC (JC-13) and SAE (CE-12) joint sub-committees as part of our continuing work to support military standard development and maintenance. These devices present various challenges to standardization given the complexities of the devices and technologies, especially as they related to historical approaches for test and validation. This paper will review the challenges being addressed by the task group.